

ABSTRACT OF THE DISCLOSURE

In order to reduce the power consumed by the circuit when the image data is transferred to a memory in a display means, a write region detecting means 8 is provided to detect the address region in the graphics memory 2 accessed for writing by the image data writing means 1, and only such data that is within the region including the addresses accessed by the writing means 1 is transferred to the memory 5 in the display means 4. The region including the accessed addresses may for example a rectangular region of from the minimum vertical direction address to the maximum vertical direction address Y among the accessed addresses, and from the minimum horizontal direction address to the maximum horizontal direction address among the accessed addresses.